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12/14/2001

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10020111	12/14/2001	257	301	2811	Booth

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438 2812

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no		TI-31016
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials			
TITLE : Methods and apparatus for inducing stress in a semiconductor device			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435 (Rev. 12-04)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Assistant Examiner		Application Examiner	
Primary Examiner			
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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